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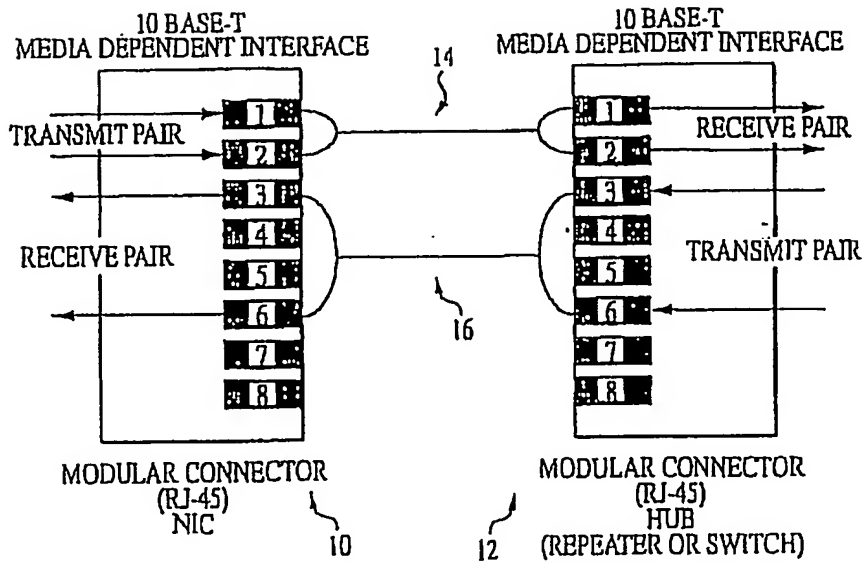


FIG. 1
(Background Method)

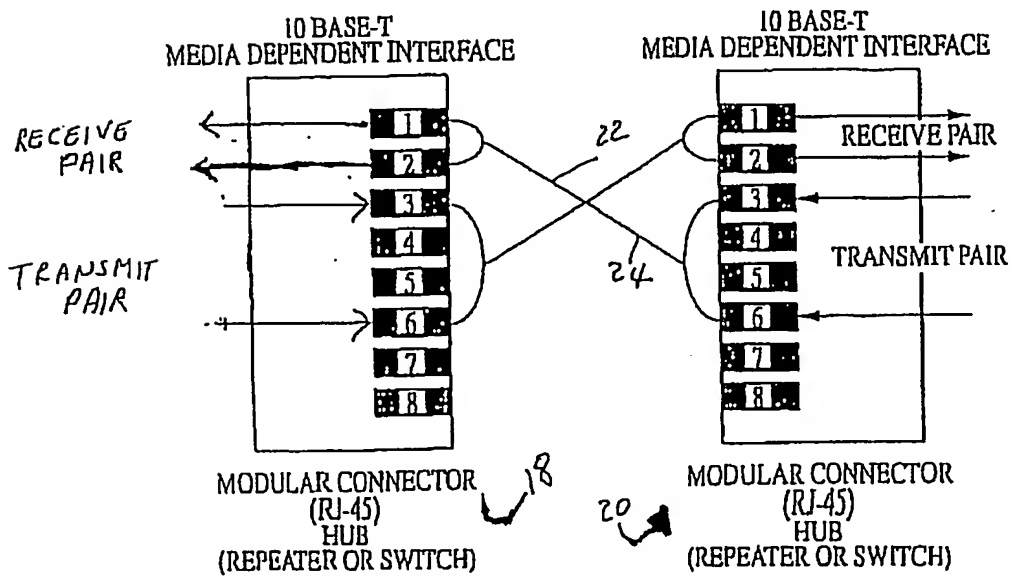


FIG. 2
(Background Method)

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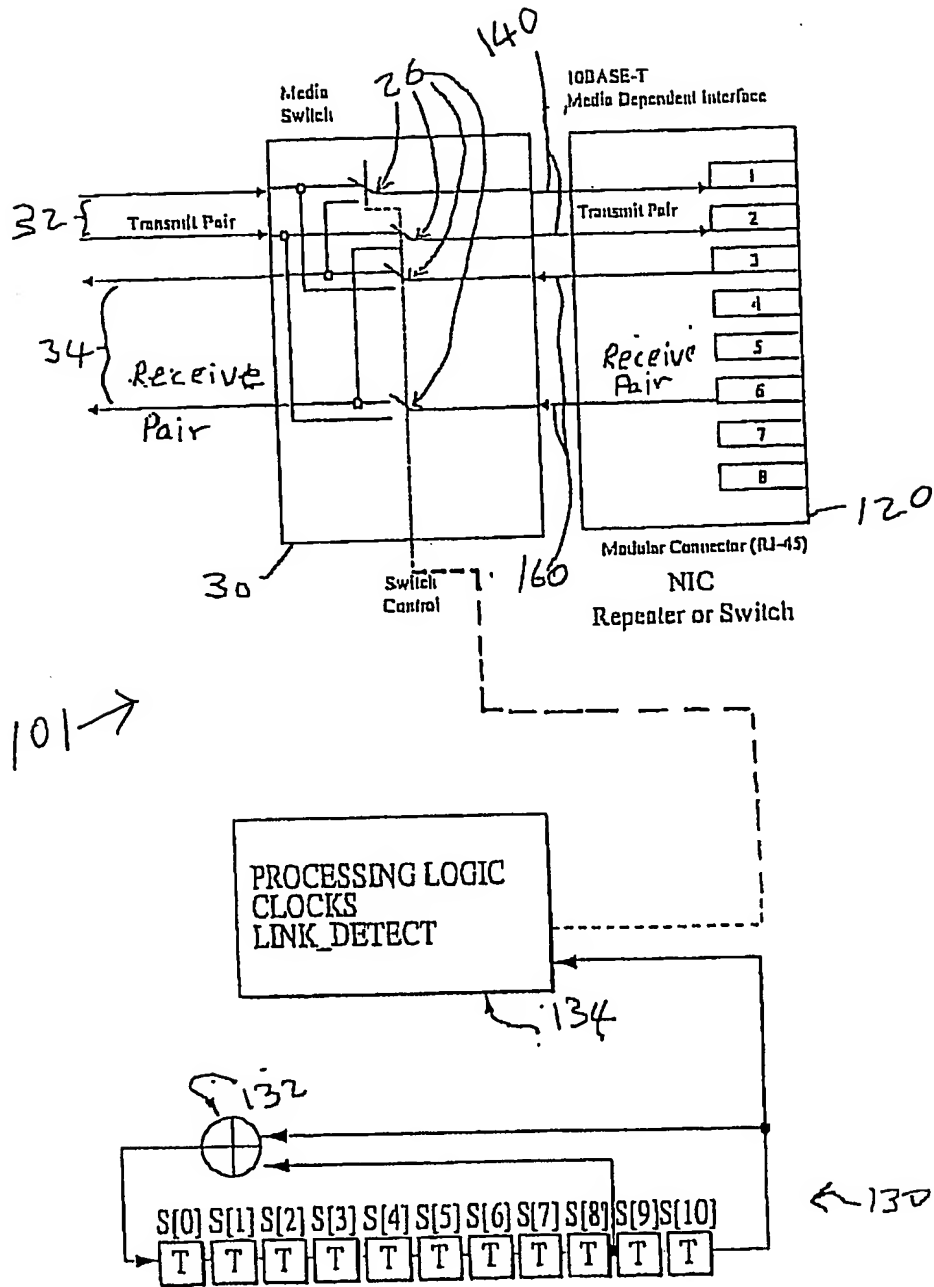
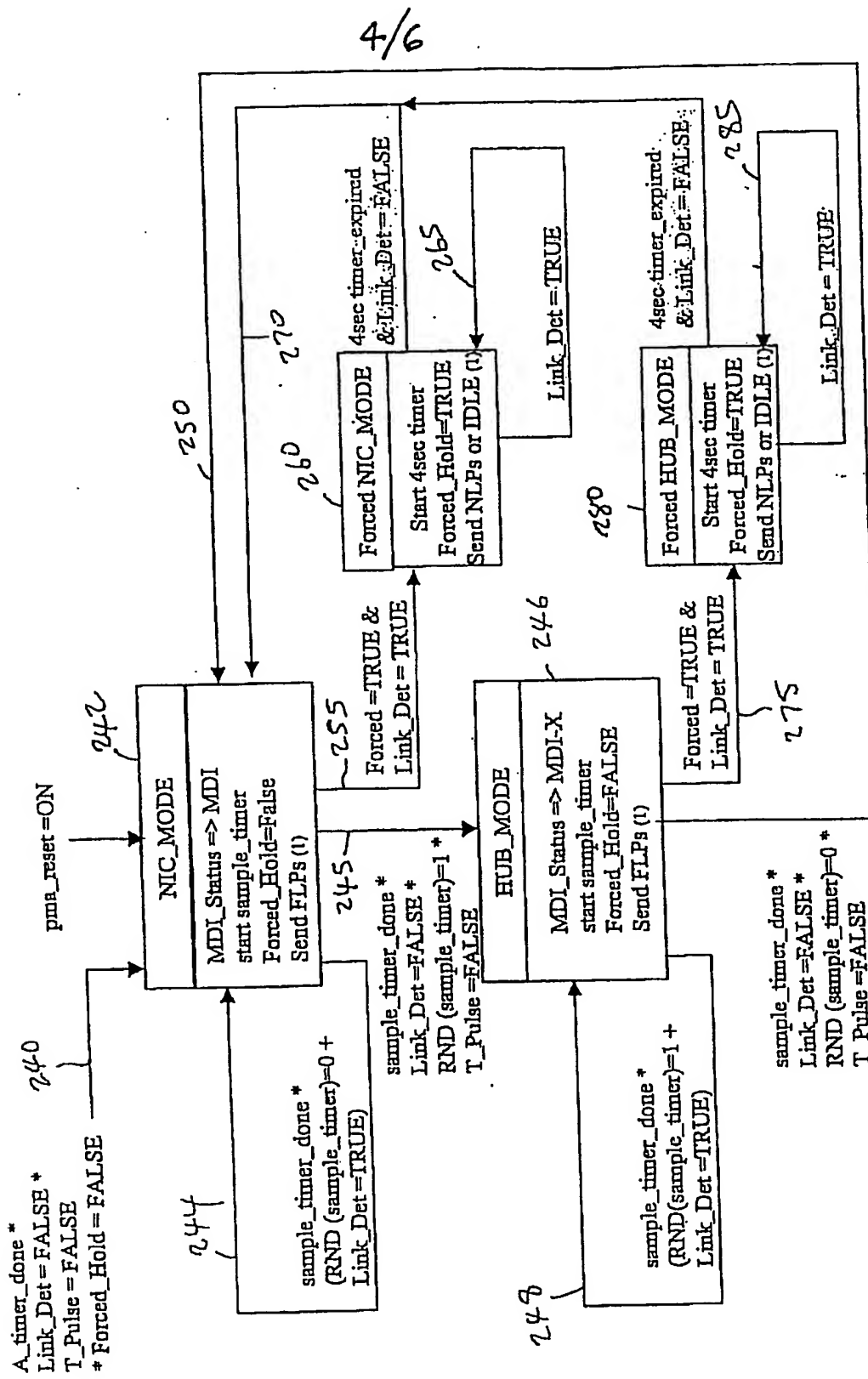


FIG. 3

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CONTACT	PHY	MDI	MDI-X
1	BI_DA+	BI_DA+ 32	BI_DB+ 34
2	BI_DA-	BI_DA- 32	BI_DB- 34
3	BI_DB+	BI_DB+ 34	BI_DA+ 32
4	BI_DC+	BI_DC+	BI_DD+
5	BI_DC-	BI_DC-	BI_DD-
6	BI_DB-	BI_DB- 34	BI_DA- 32
7	BI_DD+	BI_DD+	BI_DC+
8	BI_DD-	BI_DD-	BI_DC-

Fig. 4



Forced Mode Auto-MDIX State Machine

(1) The assertion to send FLPs or NLPs or IDLE applies only when LINK is false. Once LINK is established, DATA may be transmitted.

Figure 5

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Probability of Run Length

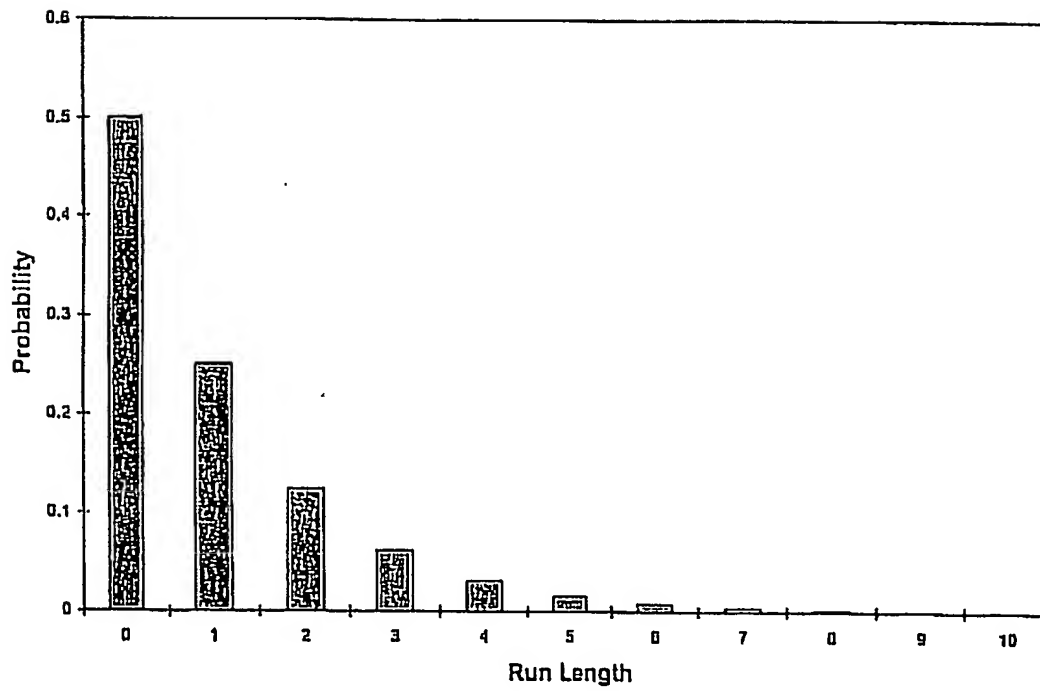


FIG. 6

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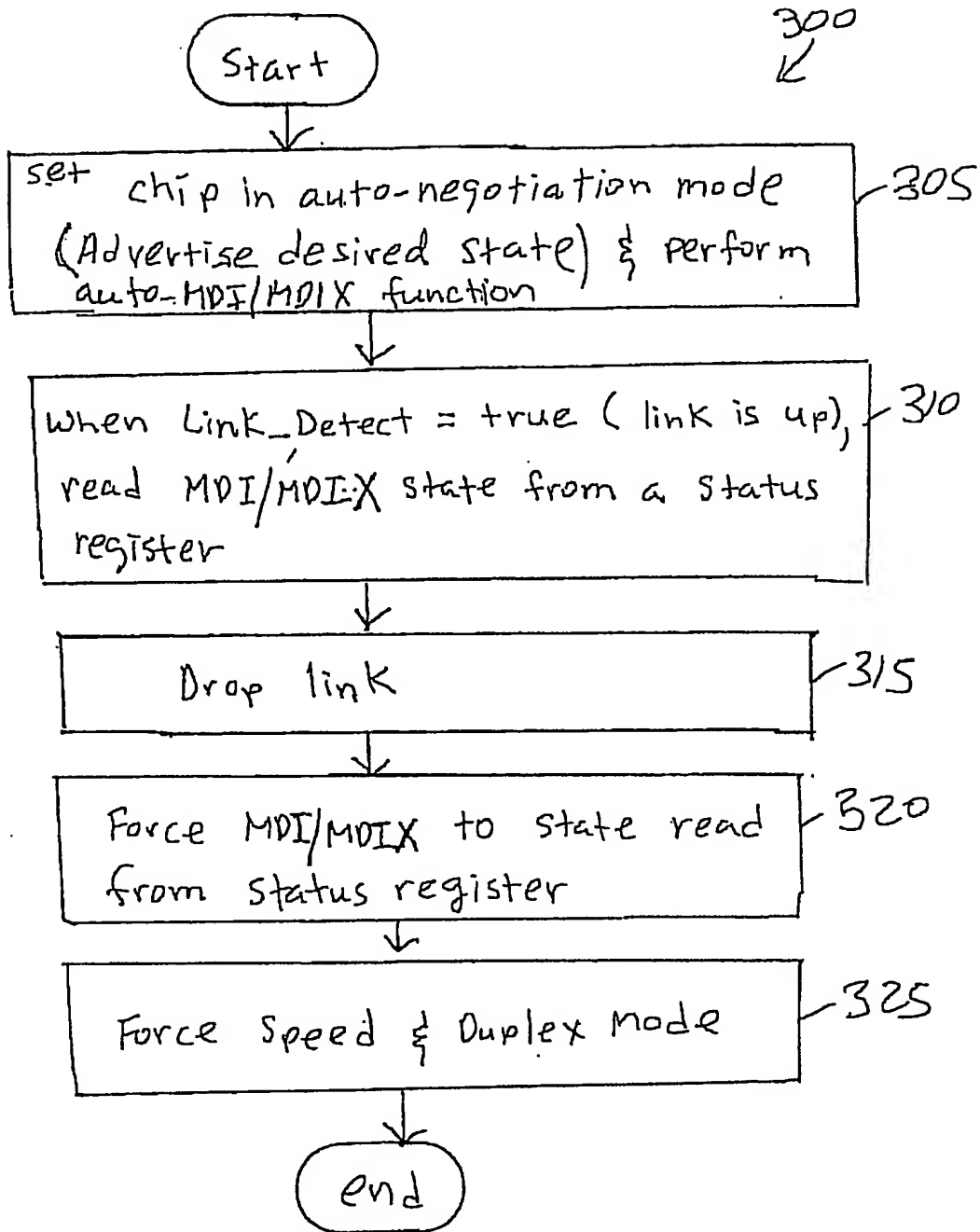


Figure 7